



Bharatiya Vidya Bhavan's
SARDAR PATEL COLLEGE OF ENGINEERING

(Government Aided Autonomous Institute)

Andheri (W) Mumbai - 400058

ELECTRICAL ENGINEERING DEPARTMENT



TIME TABLE

Academic Year: 2021-22

Term: I

SEMESTER: III (DSY)

S.Y.B.TECH (ELEC.ENGG.)

Room No. 240

	TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
1	09.00-10.00	EC (D) VJ	EC VJ	EC VJ	OCIS SS	EC VJ
2	10.00 - 11.00		DE US	EN SD	OCIS SS	AM-III VIS
3	11.00-12.00	DE US	DE (D) US	EN (D) SD	EN SD	AM-III VIS
4	12.00-01.00	DE US			EN SD	AM-III (D) VIS
5	01.00-02.00	RECESS				
6	02.00-03.00	OC	Rem	I.I.I	OCIS (D) SS	AM-III VIS
7	03.00-04.00	GT	Rem	SFS	SFS	VA
8	04.00-05.00	SC	SC	GL	GL	VA
9	05.00-06.00	SC	SC	GT	SFS	SFS

Course and Course Code			Faculty	
AM-III	Applied Mathematics-III	BS-BTE301	VIS	Prof. Vinod Sharma
OCIS	Organizational Communication and Interpersonal Skills	HSM-BTE301	SS	Prof. Shyamli Solanki
EC	Electronics Circuits	PC-BTE301/ PC-BTE305	VJ	Prof. Vidyulata Joshi
EN	Electrical Networks	PC-BTE302/ PC-BTE304	SD	Prof. Sangeeta Daingade
DE	Digital Electronics	PC-BTE303/ PC-BTE306	US	Prof. Ushma Shah

I.I.I.: Industry Institute Interaction, SFS: Self study, OC: Online course, Rem: Remedial class, GL: Guest Lecture, GT: GATE Training, VA: Value added, SC: Student Counselling

Note: Time table is for the duration 15/12/2021-15/01/2022.

18/12/2021: Monday 25/12/2021: Tuesday 01/01/2022: Wednesday 15/01/2021: Thursday timetable to be followed.

A. S. Salom
Head of the Department

S. S. Salom
Dean Academics

S. S. Salom
Principal

K. K. Salom
13/12/2021